"low battery 2" since the last time this minimum was reset (the maximum displayable value is 9999). In the case where "low battery 2" has never been reached, "----" appears in the VTBI display.

AUDIO TEST

- 1. Press COMPUTER CONTROL/MONITOR repeatedly until:
 - "Audio Test" scrolls on the Operator Information display.
- 2. Press START and check:
 - "no audio", "key", "variable", and "max" appear in the Operator Information display.
 - associated feedback voltages appear in the VTBI display.
 - an audible tone is sounded that increases in intensity as each message appears in the Operator Information display.

POWERDOWN TEST

- Press COMPUTER CONTROL/ MONITOR repeatedly until:
 - "Powerdown Test" scrolls on the Operator Information display.
- 2. Press START and check:
 - "OFF n" displays statically on the Operator Information display ('n' counts down in seconds from 3 to 1)
 - After reaching 1 the instrument shuts down.

MECHANISM ALARM CIRCUIT (M.A.C.)

Gemini PC-1 instruments are being manufactured with a pump mechanism alarm circuit (M.A.C.) to enhance detection of impact damage to the instrument as the result of dropping or other abnormal handling.

If a PC-1 pump/controller goes into an alarm condition and scrolls "HELP INTERNAL ERROR" following Power On, check the Error Code on the Central Display. These alarm conditions are

associated with either faulty AIL hardware or a disconnect in the pump mechanism alarm circuit. To determine the specific problem, perform the following troubleshooting procedures.

- a. Turn off the instrument and remove the AC power cord from the AC outlet.
- b. Separate the case (refer to Section 5.5.1).
 - Use a 5/32" Alien driver or wrench to remove the four socket head screws that connect the front and rear case assemblies. (Do not leave screws on work surface. Front panel could be damaged if front case is laid on the screws).

NOTE

When separating and positioning the front and rear cases for M.A.C. installation, ensure that no tension is applied to the harnesses connecting the case assemblies.

- c. Inspect the upper and lower M.A.C. assembly contacts on each pump mechanism for a disconnected condition.
- d. If the M.A.C. contacts (spring clips) are in place, inspect the M.A.C. wires and connectors for a cut or break.
- e. If the M.A.C. circuit is intact, then the alarm condition can be attributed to an AIL hardware problem. Follow maintenance manual procedures for troubleshooting, removal and replacement of the AIL/SCD assembly.
- f. If the M.A.C. contacts are disconnected, the instrument has been subjected to an abnormal impact condition. Visually inspect the pump mechanism(s) for severe cracks or breaks in the areas around the mounting flanges and housing pivot points. Small cracks do not affect functionality.
- g. If no visual damage is detected, attempt to move the top of the pump mechanism laterally (side to side). If the mechanism is intact, there will be very little, if any, lateral motion. If the mechanism is broken, the lateral movement will be easily discernible.

NOTE

Do not mistake movement of the top of the pump mechanism along the hinge axis as

lateral movement.

h. If the mechanism is intact, with no severe cracks or breaks, inspect the M.A.C. components for obvious damage; e.g., severely bent copper components. If all components are intact and undamaged, reconnect the upper and lower M.A.C. spring clips.

NOTE

It is important that the M.A.C. spring clips be compressed only the amount necessary to allow the contacts to be inserted into the slots on the top plate.

 If a mechanism is severely cracked or broken, or if the M.A.C. system is damaged, replace the damaged components. procedure is to perform the Maintenance Mode test that replicates the reported discrepancy; e.g., if a control key is not functioning - run the Keypad test, if a LED segment is out - run the Lamp test, if the instrument fails to power-up - check the probable causes under Initialization.

The corrective actions are listed in a descending order of failure probability. Performing the corrective actions in the sequence provided should reduce repair time and expedite returning the instrument to patient care service. If the test equipment required to troubleshoot and repair a microprocessor system is not available at your facility, it is recommended the instrument be returned to the factory for repair.

5.4 TROUBLESHOOTING

The troubleshooting routines presented in the Table 5-1 are correlated directly to the Maintenance Mode test sequence described in Section 5.3. The recommended troubleshooting

Table 5-1. Troubleshooting/Fault Isolation Guide

ž.		
Test/Fault	Probable Cause	Corrective Action
INITIALIZATION		
LEDs fail to illuminate	Battery <5.3 Volts	Connect AC Power
	Blown Fuse (Input Module)	Replace fuse
	F1 on Power Supply PCB blown POWER ON switch inoperative	Replace fuse Check Keypad Cable Connector
		Test/Replace Keypad
	NICAD Battery Failure	Replace NICAD Battery
	Digital Logic Failure	Replace Digital Logic Board
No Alarm tone	Audio Oscillator Failure	Replace Audio Oscillator
LEDs stay ON	Digital Logic Failure	Replace Digital Logic Board
VERSION DISPLAY		·
'maintenance Vx.xx vice 'PC-1 Vx.xx'	Initialized in Maintenance Mode	Reinitialize in Normal Mode
'PC-1 Vx.xx' vice 'maintenance Vx.xx'	COMPUTER CONTROL/ MONITOR switch not held during initialization	Reinitialize - hold COMPUTER CONTROL/MONITOR switch during initialization

		·
	COMPUTER CONTROL/ MONITOR switch failure	Replace Keypad
Model/Version fails to scroll	Digital Logic Failure Display Board Failure Power Supply Board Failure	Replace Digital Logic Board Replace Display Board Replace Power Supply Board
Model/Version display corrupted	Digital Logic Failure	Replace Digital Logic Board
LAMP TEST w/AUDIO	Display Board Failure	Replace Display Board
LED segment fails to illuminate	Display Board Failure Digital Logic Failure	Replace Display Board Replace Digital Logic Board
No Audio adjust	Audio Control Pot. Failure	Replace Audio Control Pot.
KEYPAD TEST		
Key/Display Mismatch or Invalid Key	Keypad Failure	Replace Keypad Assembly
	Display Board Failure	Replace Display Board
ERROR LOG DISPLAY (See Tal	ble 5-2, 2a or 2b for a listing and desc	ription of Error Log Codes)
MOTOR HOMING TEST		
Motor Fails to Home to selected position	Motor Harness Disconnected	Reconnect Motor Harness
±1 step	Digital Logic Failure	Replace Digital Logic Board
	Motion Sensor Harness Disconnected	Reconnect Motion Sensor Harness
	Motion Sensor Failure	Replace Motion Sensor
PUMP TEST (Allows pumping mech	anism to be operated without Alarm st	oppage)
SERIAL PORT TEST	 	
"echo" test fail	Faulty Communication Plug	Replace Comm Emulator Plug
	Digital Logic Board Failure	Replace Digital Logic Board
	Power Supply Board Failure	Replace Power Supply Board
A/D VOLTAGE DISPLAY		
"strain" reading >'0', set not installed	Strain Beam Out of Calibration	Recalibrate Strain Beam (see Section 5.7)
	Strain Beam Failure	Replace Strain Beam
	Digital Logic Board Failure	Replace Digital Logic Board

"strain" reading <100' or >200' with dry pumping segment	Strain Beam Out of Calibration	Recalibrate Strain Beam
installed	Strain Beam Failure	Replace Strain Beam
	Digital Logic Board Failure	Replace Digital Logic Board
"sys batt" reading <'279' or >'355'	Power Supply Board Failure	Check Battery Voltage at in-line fuse
		Replace Power Supply Board
	Digital Logic Board Failure	Replace Digital Logic Board
	Battery Failure	Replace Battery
	Wrong Battery Installed	Install IPB Listed Battery
"V(mains) reading <'245' or >'255' (AC connected)	Power Supply Board Failure	Replace Power Supply Board
> 255 (AC connected)	Digital Logic Board Failure	Replace Digital Logic Board
"V(ref)" other	Power Supply Board Failure	Replace Power Supply Board
than '249'±05%	Digital Logic Board Failure	Replace Digital Logic Board
"V(audio)" normally 0	N/A	
"V(NVRAM)" reading <'246' or >'328'	NiCad Battery Failure	Recharge NiCad Battery
> 320		Replace NiCad Battery
	Digital Logic Board Failure	Replace Digital Logic Board
	Power Supply Board Failure	Replace Power Supply Board
INPUT PORT TEST		
'Normal' mode	See Section 5.3.2 Maintenance Mode expected readout in Normal and Self NOTE	
	The logic for the AlL and ECD sen other sensors (ECD sensors applic Consequently, in the Normal mode are being tested for response to the sensor operation. The following Altests indicate the response expect 'Selftest' mode.	cble to 110V only, except V8.12). test the AIL and ECD sensors e processor strobe rather than iL and ECD sensor operation
AIL Sensor - wrong digit for condition	Ultrasonic Emitter/Receiver failure	Replace AIL/SCD Assembly
	Analog Circuit Failure	Replace AIL/SCD PC Board

	Digital Logic Board Failure	Replace Digital Logic Board				
only (except V8.12). ECD Sensor - wrong digit for condition	Light Emitter/Receiver Failure	Replace ECD				
Condition	Communication Cable Failure	Reconnect or Replace Commication Cable				
	Power Supply Board Failure	Replace Power Supply Board				
	Digital Logic Board Failure	Replace Digital Logic Board				
SCD Sensor - wrong digit for condition	Light Emitter/Receiver Failure	Replace AIL/SCD Assembly				
Condition		Analog Circuit Failure Replace AIL/SCD PC Board				
	Digital Logic Failure	Replace Digital Logic Board				
Motion Sensor - wrong digit for condition	Sensor Failure	Replace Sensor				
wrong digit for condition	Digital Logic Board Failure	Replace Digital Logic Board				
Door Sensor - wrong Digit for condition	Sensor Failure	Replace Sensor				
Digit for Condition	Digital Logic Board Failure	Replace Digital Logic Board				
	NOTE					
	In the 'Selftest' mode, the microprocessor is strobing the sensors in accordance with a software protocol. The digital presentation seen in the RATE and VTBI displays reflects the sensor response to the strobe. If the response is not the expected response, a problem exists within the strobe circuitry.					
RAM Display	For IMED Engineering use only					
POWER DOWN TEST						
Displays remain On	Digital Logic Board Failure	Replace Digital Logic Board				

Table 5-2. PC-1 Error Log Codes (V2.xx, V5.xx, V6.XX and V6.3x/4x)

The Error Codes listed below represent the results of software initiated subsystem tests. The tests are evaluated on Pass/Fail logic with an error code generated for a fail condition.

00 00 01 03 04	NOT USED Error Log ROM NVRAM Software Release V6.3x/4x RAM	Meaning Occurs only during startup; the Error Log is reset resulting in loss of resident error log entries. Detected during power-up; instrument fails CRC check and powers down immediately WITHOUT alarm. Detected during power-up. The portion of RAM subjected to a CRC test fails. Failure results in loss of previously selected infusion parameters. Default parameters display. Not Implemented	Logic Board Battery Circuit Check voltage at RAM VCC: If: <2.0V - NICAD battery failure Logic Board Battery excessively discharged. Attempt recharge for 4 hours Logic Board Logic Board Logic Board
050	Critical parameters out of range Software Release V6.xx Software Release V6.3x/4x	performed on RAM segments not related to infusion parameters. Instrument fails this validity check and powers down WITHOUT alarm. During power-up a range check is performed on infusion parameters stored in NVRAM. Failure of this check results in loss of previously selected infusion parameters. Default parameters are displayed. If a variable is out of range the following occurs: a transparent 9x error code is logged, "HELP INTERNAL ERROR" scrolls, an audio alarm sounds, NVRAM values revert to default, all keys except PAUSE/STOP are disabled.	Logic Board Battery Circuit (see Error Code #1)

Battery excessively discharged. Attempt recharge for 4 hours Battery Circuit (see Error Code #1) Logic Board	Logic Board	Display Board Logíc Board		Logic Board	Pumplng Mechanism	
During power-up voltage is checked; measurements >8.0 or <≈5.7 VDC cause the instrument to immediately power down WITHOUT Alarm.	Occurs during power-up battery check or any subsequent A/D conversion. An A to D interrupt is programmed upon completion of A/D readings. Failure to detect this interrupt within a pre-determined time frame will cause the instrument to lock up and display the error code in the VTBI display.	Occurs when a keycode is received that is outside of the legal keycode range. An error is logged, "HELP INTERNAL ERROR" scrolls, audio alarm sounds, pumping stops, all keys except PAUSE/STOP are disabled and error code is displayed in VTBI.		Occurs when a runaway program is detected during a routine check of software logic. "HELP INTERNAL ERROR" scrolls, the Error Code is displayed in the VTBI, audio alarm sounds and all keys except PAUSE/STOP are disabled.	Occurs when an error >1.5% in a sample of 50 motor revolutions is detected by the motion sensor. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code is displayed in VTBI and all keys except PAUSE/STOP are disabled.	
Battery	A/D	Invalid Key	NOT USED	Software Release V2.xx General Software Error Software Release V5.xx NOT USED Software Release V6.xx and V6.3x/4x General Software Error	Motor Sync Off	NOT USED
90	07	08	09-11	12	13	4

Logic Board Pumping Mechanism		Battery Capacity Diminished Battery Charger Circuitry Power Supply Board	Normal power-down sequence must be activated to reset instrument.	Logic Board Door Harness Assembly		AIL Board AIL/Door Harness Logic Board	
Occurs 120 motor steps after the motion sensor fails to confirm motor sync. The motion sensor is inoperative or the motor is not turning. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.		After ≥10 hours of continuous operation on AC power, instrument must operate for ≥2.5 hours on battery; if unable, a fast battery discharge condition occurs: "HELP BATTERY" scrolls, audio alarm sounds, error codes 18 and 38 are logged, error code 38 displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Occurs when the instrument is powered-down without using the PAUSE/STOP control (i.e. Watchdog or battery failure). During the next power-up, the instrument will enter an Internal Error condition: "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code is logged and displayed in VTBI and all keys except PAUSE/STOP are disabled.	Occurs when the microprocessor detects a failure of the door sensor. "HELP INTERNAL ERROR" scrolls, audio afarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.		Occurs when the microprocessor detects a failure of the AIL sensor. "HELP INTERNAL ERROR" scrollS, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	
No Sync Flag detected	NOT USED	Software Release V6.3x/4x Fast Battery Discharge	Software Release V6.3x/4x Improper Power-Down	Door Alarm	NOT USED	AlL Alarm	NOT USED
د	16-17	18	5-16	20	21	22	23-25

Power Supply Board		Logic Board	Logic Board			Logic Board		Logic Board	
Occurs when battery voltage >8.0 VDC is detected during normal instrument operation. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.		Occurs when a failure of the CRC check of ROM is detected during normal instrument operation. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Occurs when a range check of RAM infusion parameters detects an out-of-range condition. "HELP INTERNAL ERROR" scrolls and the Error Code No. appears in the VTBI display.			Occurs when the main processor, through an A/D channel, Is unable to read a 2.5V reference within ±5%. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Tolerence for reading 2.5V reference is ±12%	Occurs when the main processor fails to detect at least 0.2V on an A/D channel following audio circuitry activation. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping continues and all keys except PAUSE/STOP are disabled.	
Battery Overcharge	NOT USED	ROM CRC	Software Release V2.xx Insanity Software Release V5.xx NOT USED	Software Release V6.xx and V6.3x/4x NOT USED	NOT USED	V Ref	Software Release V6.xx and V6.3x/4x	Audio	NOT USED
26	27	28	29		30-34	35		36	37

Battery excessively discharged. Attempt recharge for 4 hours Lead Acid Battery Power Supply Board		Improper Power-down NVRAM Battery Logic Board	Improper Power-down NVRAM Battery Logic Board	Improper Power-down NVRAM Battery Logic Board	Improper Power-down NVRAM Battery Logic Board	Improper Power-down NVRAM Battery Logic Board	Improper Power-down NVRAM Battery Logic Board
Occurs when an A/D converter reads a battery voltage below ≈5.4V. "HELP BATTERY" scrolls, audio alarm sounds, pumping stops, error code displays on VTBI and all keys except PAUSE/STOP are disabled.		Error Log partition of partitioned NVRAM experienced a CRC failure between powerdown and power-up. Variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	Fixed Biotech Setup partition of partitioned NVRAM experienced a CRC failure between power-down and power-up. Variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	Non-Critical State partition of partitioned NVRAM experienced a CRC failure between power-down and power-up. Variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	Non-Critical Data partition of partitioned NVRAM experienced a CRC failure between power-down and power-up. Variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	TPN Data partition of partitioned NVRAM experienced a CRC failure between powerdown and power-up. Variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	Critical State partition of partitioned NVRAM experienced a Validity check failure between power-down and power-up. Effected variables in effected partition are initialized to default values and error code is logged. Instrument is usable.
Low Battery II Error	NOT USED	Software Release V6.3x/4x Error Log NVRAM Variables	Software Release V6.3x/4x Fixed Biotech Setup NVRAM Variables	Software Release V6.3x/4x Non-Critical State NVRAM Variables	Software Release V6.3x/4x Non-Critical Data NVRAM Variables	Software Release V6.3x/4x TPN Data NVRAM Variables	Software Release V6.3x/4x Critical State NVRAM Variables
38	39	40	1.4	42	43	44	45

re Release V6.3x, 4x NVRAM experienced a Validity check failure les between power-down and power-up. Effected variables in effected partition are initialized to default values and error code is logged. Instrument is usable.	NOT USED	1.3x/4x Communication failures. "HELP INTERNAL Communication failures." HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	NOT USED	re Releases This error code is related only to software Not hardware related, no action required diagnostics.	re Release V6.xx 3.3.44x NOT USED	During power-up A/D converter checks 2.3x/4x audio fransducer Input voltage to be audio during audio activity. If not "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	NOT USED	A Battery NVRAM battery voltage is <2.4 VDC for a specified period, then: "HELP BATTERY" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	At power-up logic board processor Checks version number of display Checks version number of display I/Software Version processor software. If a mismatch: "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
Software Release V6.3x14x Critical Data NVRAM Variables	NOT USED	Software Release V6.xx and V6.3x/4x IPC Synchronization	NOT USED	Software Releases V2.xxV5.xx Three bad messages (IPC)	Software Release V6.xx and V6.3x/4x NOT USED	Software Release V6.xx and V6.3x/4x Power-up Audio	NOT USED	NVRAM Battery	Software Release V6.xx and V6.3x/4x EPROM/Software Version Mismatch
46	47-58	59	09	61		62	63	64	65

1st occurance, recycle and ignore Logic Board Display Board	Display Board		Display Board	Error is possible under normal conditions, but should not be common. If repeated occurance: Logic Board Display Board	Strain Beam Logic Board	Sticking pumping mechanism Logic Board
Critical values (rate, VTBI and/or language) in display processor are checked for legal range. If out: "HELP INTERNAL EAROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Display processor detects an unacceptable voltage level on a 7 segement display. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.		Display processor startup RAM test has failed. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Display processor has received three IPC messages in a row from the 8096 containing a bad CRC. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Occurs when the main processor does not detect ≥100 mV variance between the highest and lowest readings during 2 revolutions of pumping mechanism. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Actual time required to complete a pumping mechanism revolution differs from calculated value by ±12% for 3 revolution sample. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
Slave data error	Slave segment error	NOT USED	Slave RAM error	Slave IPC CRC error	Strain beam error	Motor revolution error
99	67	68	69	70	7.1	72

______ 5-20 ____

Logic Board	Logic Board	Logic Board	
Value used to calculate motor tables does not = redundancy check value. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Value of calculation error on motor table >200msec. Implies a processor failure. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Runtime working RAM failure, 8096 side. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	
Rate corruption error	Motor table calculation error	Stuck bits error	NOT USED
73	74	75	62-92

The following startup and runtime errors result from NVRAM insanity. In software release <u>V2.xx/V6.xx</u> instruments the error codes will be lost if the NVRAM purges itself following an insanity condition. In software release <u>V6.3x/4x</u> instruments the insanity error code will be retained. In the event one of these errors is displayed: check NVRAM battery voltage after charging battery for ≥8 hours; if low, replace battery; if within limits, replace Logic Board.

Meaning	NOT USED	Current rate out of range	Piggyback rate out of range	Current VTBI out of range	Piggyback VTBI out of range	Total Volume Infused out of range	Secondary Volume Infused out of range	Motor step number out of range	RAM copy of ROM-CRC is in error	Error in Rate. VTBI. etc. for fractional mode
Runtime	96	91	92	93	94	92	96	26	96	66
Power-up / Runtime	/ 08	81	82 /	83	84 /	85 /	/ 98	/ 28	/ 88	/ 68

Table 5-2a. PC-1 Error Log Codes (V7.xx)

The Error Codes listed below represent the results of software initiated subsystem tests. The tests are evaluated on Pass/Fail logic with an error code generated for a fail condition.

oring oldedon	Denote of the second of the se	rror Log is Logic Board	Battery Circuit Check voltage at RAM VCC after charging battery for ≥8 hours: If: <2.0V - NICAD battery failure			AM test is Logic Board related to fails this WITHOUT		is checked; Battery excessively discharged. Attempt PC cause the recharge for 4 hours. power down Battery Circuit (see Error Code #1)	
Mossiss		Occurs only during startup; the Error Log is	entries.			During power-up a destructive RAM test is performed on RAM segments not related to infusion parameters. Instrument fails this validity check and powers down WITHOUT alarm.		During power-up voltage is checked; measurements > 8.0 or < 5.15 VDC cause the instrument to immediately power down WITHOUT alarm.	Occurs during power-up battery check or any subsequent A/D conversion. An A to D interrupt is programmed upon completion of A/D readings. Failure to detect this interrupt within a pre-determined time frame will cause the instrument to lock up and display the error code in the VTBI display.
Description				FOR PC-1	FOR PC-1		FOR PC-1		
_	NOT LISED	Error Log		RESERVED 6.XX	RESERVED INTEGER	RAM	RESERVED INTEGER	Battery	A/D
Code No	00	01		02	03	04	05	90	70

					,		
Display Board Logic Board	Logic Board	Pumping Mechanism Logic Board Power Supply Board		Logic Board Power Supply Board Pumping Mechanism			Battery Capacity Diminished Battery Charger Circuitry Power Supply Board
Occurs when a keycode is received that is outside of the legal keycode range. An error is logged, "HELP INTERNAL ERROR" scrolls, audio alarm sounds, pumping stops, all keys except PAUSE/STOP are disabled and error code is displayed in VTBI.	During power-up, a checksum value is calculated for the four ROM banks. If the value does not match a precalculated "correct" value, a corruption of ROM is suspected, and the instrument is shut down WITHOUT alarm.	Occurs when an error >1.5% in a sample of 50 motor revolutions is detected by the motion sensor. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code is displayed in VTBI and all keys except PAUSE/STOP are disabled.		Occurs 120 motor steps after the motion sensor fails to confirm motor sync. The motion sensor is inoperative or the motor is not turning. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.			After ≥1 hour of continuous operation on AC power, instrument must operate for >1/2 the charged time on battery; if unable, a fast battery discharge condition occurs: "HELP BATTERY" scrolls, audio alarm sounds, error codes 18 and 38 are logged, error code 38 displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
Invalid Key	ROM Bank [0, 1, 2, 3] - Checksum	Motor Sync Off	RESERVED FOR PC-2 TITRATION	No Sync Flag detected	RESERVED FOR PC-2 TITRATION	RESERVED FOR PC-2 INTEGER	Fast Battery Discharge
08	09-12	13	14	15	16	17	18

= 5-23 **=**

19	Improper Power-Down	Occurs when the instrument is powered-down	Normal power-down sequence must be
		without using the PAUSE/STOP control (i.e. Watchdog or battery failure). During the next power-up, the instrument will enter an Internal	activated to reset instrument. Logic Board
		Error condition: "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code is	Power Supply Board
		logged and displayed in VTBI and all keys except PAUSE/STOP are disabled.	Battery
50	Door Alarm	Occurs when the microprocessor detects a failure of the door sensor. "HELP INTERNAL	Logic Board
		ERROR" scrolls, audio alarm sounds, error code displays in VTBI pumping stops and all	Door Harness Assembly
		keys except PAUSE/STOP are disabled.	
21	RESERVED FOR PC-2		
22	AIL Alarm	Occurs when the microprocessor detects a	AlL Board
	-	FRHOR" scrolls, audio alarm sounds, error	AlL/Door Harness
= 5-		code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Logic Board
23	RESERVED FOR PC-2 TITRATION		
24-25	Inverse Flag Errors	During power-up or runtime, important	Logic Board
		variables are checked against their inverse copies to verify that no RAM bit corruption	
		has taken place. If any of the inverse flags are incorrect. 'HELP INTERNAL FREOR"	
		scrolls, "24" or "25" is displayed in VTBI, "0"	
		except PAUSE/STOP are disabled.	
56	Battery Overcharge	Occurs when battery voltage >8.0 VDC is detected during normal instrument operation.	Power Supply Board
		"HELP INTERNAL ERROR" scrolls, audio	
		pumping stops and all keys except	
		PAUSE/STOP are disabled.	
27	RESERVED FOR PC-2 TITRATION		
	_		

Logic Board	Battery Capacity diminished Battery Charger circuitry Power Supply board			Logic Board	Logic Board	Logic Board	Logic Board	
All unused ROM is protected from execution through illegal ROM space. If there is an illegal ROM access failure, the instrument logs the error code and enters a watchdog condition.	After a minimum charge time, the instrument moves from Low Battery Level 1 to Low Battery Level 1 to Low 29 is logged, and the instrument enters a Low Batt II condition (see Error Code 38).			During ROM Bank switching, a ROM stack stores the history of which ROM to return to If this stack overflows, or the integrity is corrupted, Error Code 33 is logged and the instrument enters a watchdog condition.	During ROM Bank switching, a ROM stack stores the history of which ROM to return to. If this stack is empty or the integrity is corrupted, Error Code 34 is logged and the instrument enters a watchdog condition.	Occurs when the main processor, through an A/D channel, is unable to read a 2.5V reference within ±12%. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays in VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Occurs when the main processor falls to detect at least 0.2V on an A/D channel following audio circuitry activation. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping continues and all keys except PAUSE/STOP are disabled.	
Illegal ROM Access Failure	Short LB1 to LB2 Time	RESERVED FOR PC-2 TITRATION	RESERVED FOR PC-2 INTEGER	ROM Stack Push Error	ROM Stack Pop Error	V Ref	Audio	RESERVED FOR PC-2 TITRATION
28	29	30	31-32	33	₹ 5-25	35	36	37

Attempt				
discharged.				
Battery excessively recharge for 4 hours Lead Acid Battery Power Supply Board	Logic Board	Logic Board		
Occurs when an A/D converter reads a battery voltage below ≈5.15V. "HELP BATTERY" scrotls, audio alarm sounds, pumping stops, error code displays on VTBI and all keys except PAUSE/STOP are disabled.	Occurs when the normal calling sequence in the main software loop becomes corrupted, or a case statement index is corrupted. The error code is logged "HELP INTERNAL ERROR" scrolls, "39" displays in VTBI and "0" in RATE, audio alarm sounds, the instrument stops pumping, and all keys except PAUSE/STOP are disabled.	The non-volatile Random Access Memory (NVRAM) is divided into seven partitions, each of which has a validity check performed on it at power-up. If this check fails, a corruption of NVRAM is suspected. The code is logged, all variables in the affected NVRAM partition are initialized to default values, and the instrument is ready for normal use.	The codes associated with each partition of NVRAM variables are: 40 41 42 Fixed Biotech Setup 42 Non-critical State Non-critical Data 44 44 5 Critical State 645 45 47 AutoTaper	
Low Battery II Error	General Software Error	Partition Specific NVRAM failure		RESERVED FOR PC-2
38	36	5-26		48-49

Logic Board		Logic Board	Logic Board NiCad Battery	Logic Board	Power Supply Board Battery
When the logic processor is reset, ROM bank 0 should be the first bank accessed; if bank 1, 2, or 3 is accessed first, a ROM bank reset error has occurred and the error code is logged, "ROMx rst" will display, the audio alarm is sounded, and the instrument enters a watchdog state.		If the controller state is found to be out of range, the error code is logged, "HELP INTERNAL ERROR" displays, "54" displays in VTBI and "0" in RATE, the audio alarm is sounded, the instrument stops pumping, and all keys except the PAUSE/STOP are disabled.	The NiCad circuitry is periodically validated; if a circuit failure condition is sensed, the error code is logged, "HELP INTERNAL ERROR" displays, "55" shows in VTBI and "0" in RATE, the audio alarm is sounded, the instrument stops pumping, and all keys are disabled except the PAUSE/STOP key.	The Event History NVRAM partition has a Cyclic Redundancy Code (CRC) calculation or a validity check performed on it at powerup. If a CRC result does not match the previous result, or the validity check fails, a corruption of NVRAM is suspected. The code is logged, all variables in the affected NVRAM partition are initialized to default values, and the instrument is ready for normal use.	The Dual Charger timer variable is periodically range-checked; if out-of-range, the error code is logged. No audible alarm.
ROM Bank [1,2,3] неset Error	NOT USED	Controller State Error	NiCad Circuit failure	Event History NVRAM failure	Dual Charger Error
50-52	53	54	1G 1G	56	57

Logic Board	Logic Board Display Board	Logic Board Display Board	Logic Board	Logic Board
Audio software is common to both PC-1 and PC-2 (2 channel). If the PC-1 attempts to access the non-existent Channel B (existent only in the PC-2), the error code is logged, "HELP INTERNAL ERROR" displays, "58" shows in VTBI and "0" in RATE, audio alarm is sounded, the instrument stops pumping, and all keys are disabled except PAUSE/STOP. The instrument may enter a watchdog condition.	Unacceptable level of inter-processor communication failures. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	If the logic processor determines that the display processor has been silent for 2 seconds, or if a number of IPC errors have been accumulated, the error code is logged, "HELP INTERNAL ERROR" displays, "60" shows in the VTBI, audio alarm is sounded, the instrument stops pumping, and all keys are disabled except PAUSE/STOP.	The variable which indicates which error type has occurred is range checked before the error is logged. If the error value is found to be out of range, the value is forced to the illegal error code value. The error code is logged, "HELP INTERNAL ERROR" displays, "61" shows in the VTBI, audio alarm is sounded, the instrument stops pumping, and all keys are disabled except PAUSE/STOP.	During power-up A/D converter expects audio transducer input voltage to be >0.2VDC during audio activity. If not, "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
Audio Channel Error	IPC State Error	IPC Message Error	Illegal Error Code	Power-up Audio
58	59	⊕ 5-28 =	6	62

Logic Board	Logic Board NiCad Battery	Wrong Display EPROM Wrong Logic EPROM	1st occurance, recycle and ignore Logic Board Display Board	Display Board	Logic Board	Display Board
The ROM bank ID number of the intended destination ROM bank is not legal. The error code is logged and the instrument enters a watchdog condition.	NVRAM battery voltage is <2.4 VDC for a specified period, then: "HELP BATTERY" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	At power-up togic board processor checks version number of display processor software. If a mismatch: "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Critical values (rate, VTBI and/or language) in display processor are checked for legal range. If out of range: "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Display processor detects an unacceptable voltage level on a 7 segment display. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	The TPN timekeeping variables are safety checked against inverted duplicate copies. If there is a mismatch, the error code is logged, "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Display processor startup RAM test has failed. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
lllegal ROM Bank Reन्पest	NVRAM Battery	EPROM/Software Version Mismatch	Slave data error	Slave segment error	TPN Time Inverse Error	Slave RAM error
63	64	65	99	67	68	69

5-29

If repeated occurance; Logic Board Display Board	Strain Beam Logic Board	Sticking pumping mechanism Logic Board	Logic Board	Logic Board	Logic Board
Display processor has received three IPC messages in a row from the 8096 containing a bad CRC. "HELP INTERNAL ERROH" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Occurs when the main processor does not detect ≥100 mV variance between the highest and lowest readings during any 2 revolutions of pumping mechanism. "HELP INTERNAL EAROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Actual time required to complete a pumping mechanism revolution differs from calculated value by ±12% for 3 revolution sample. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Value used to calculate motor tables does not equal redundancy check value. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Value of calculation error on motor table >200msec. Implies a processor failure. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	Runtime working RAM failure, 8096 side. "HELP INTERNAL ERROR" scrolls, audio alarm sounds, error code displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.
Slave IPC CRC error	Strain beam error	Motor revolution error	Rate corruption error	Motor table calculation error	Stuck bits error
20	71	72	-30 	74	75

Logic Board	Logic Board		In the event one of these errors is displayed: check NVRAM battery voltage after charging battery for >8 hours; if low, replace battery; if within limits, replace Logic Board.	Logic Board
The motor speed variable is range-checked to prevent divide-by-zero errors. If detected, the error code is logged, "HELP INTERNAL ERROR" scrolls, audio alarm sounds, "76" displays on VTBI, pumping stops and all keys except PAUSE/STOP are disabled.	The power-down software checks to make sure that an orderly sequence of events has occurred. If not, then the error code is logged, the audio alarm sounds, and the instrument enters a watchdog condition.		The following startup and runtime errors result from NVRAM insanity. The specific error code is logged, "HELP INTERNAL ERROR" displays, code displays in VTBI, audio alarm is sounded, all keys are disabled except the PAUSE/STOP key. Pwr-up Runtime NersaTaper data error 81 91 Current rate out of range 82 92 Piggyback rate out of range 83 93 Current VTBI out of range 84 94 Piggyback VTBI out of range 85 95 Tot Vol Infused out of range 86 96 Sec Vol Inf out of range 87 97 Motor step number out of range 88 98 99 Error in Rate, VTBI, etc. for MICRO mode	If the runtlme CRC does not match a "correct" CRC stored value, a corruption of ROM is suspected, and the specific error code is logged, "HELP INTERNAL ERROR" displays, code displays in VTBI, audio alarm is sounded, instrument stops pumping, all keys are disabled except the PAUSE/STOP key.
Motor Speed (divide ∠y 0) Error	Insane power-down Error	NOT USED	Detailed Insanity Errors	ROM Bank [0, 1, 2, 3] CRC Fallure
76	7.7	78-79	66-06 68-08 5-31	100-103

Logic Board	Logic Board		Logic Board	Logic Board		Logic Board	
Important AutoTaper values are checked to see that they are within their legal ranges. If not, the following error codes indicate which data is corrupted, and the specific error code is logged, "HELP INTERNAL ERROR" displays, the code displays in VTBI, audio alarm is sounded, all keys are disabled except the PAUSE/STOP key.	Important AutoTaper contexts are checked to see that they are within defined contexts. If not, the following error codes indicate which context is in error, and the specific error code is logged, "HELP INTERNAL ERROR" displays, the code displays in VTBI, audio alarm is sounded, all keys are disabled except the PAUSE/STOP key.		The RATE and VTBI displays are checked for reasonable values. If checks reveal invalid data, the code is logged, "HELP INTERNAL ERROR" displays, "119" displays in VTBI, audio alarm is sounded, the instrument stops pumping, and all keys are disabled except the PAUSE/STOP key.	When ROM bank switching is in progress, the procedure is checked and validated. If out-of-range, the specific error code is logged and the instrument enters a watchdog condition.		The ROM bank ID number of the Intended destination ROM bank does not match; the specific error code is logged and the instrument enters a watchdog condition.	
AutoTaper Data insanity Errors	AutoTaper Context Errors	NOT USED	Display Numbers Error	Illegal ROM Bank [0, 1, 2, 3] Procedure Call	NOT USED	Failed to Reach ROM Bank [0, 1, 2, 3]	NOT USED
104-106	107-110	111-118	6- 	120-123	124-125	126-129	130-255

Table 5-2b. PC-1 Error Log Codes (V8.xx)

The Error Codes listed below represent the results of sub-system tests initiated by software. The tests are evaluated using Pass/Fail logic with an error code generated for a fail condition.

Unless otherwise indicated, all error codes result in malfunction I/O, i.e., (1) the appropriate error code is recorded in the error log, (2) "HELP INTERNAL ERROR" is scrolled, (3) audio alarm is generated, (4) pumping is stopped, (5) all keys except PAUSE/STOP are disabled, and (6) '0' and the error code are displayed in the rate and VTBI LEDs, respectively. A malfunction condition is terminated by powering the instrument down using the PAUSE/STOP key.

Probable Cause		Logic Board Battery Circuit (Check voltage at RAM VCC after charging battery for at least 8 hours: if less than 2V, a NiCAD battery failure is indicated)		Battery excessively discharged. Attempt recharge for 4 hours. Battery Circuit Logic Board	Logic Board	Logic Board	Logic Board	Pumping Mechanism Logic Board Power Supply Board
Meaning		Marks a clearing of the error log. Initialization occurs (1) at the time of instrument manufacture and (2) when corruption of the error log is detected (typically due to a low battery condition). This error does not result in alarm I/O.		Occurs when power-up testing of the system battery reveals voltages greater than 8 VDC or less than 5.15 VDC; causes the instrument to immediately power down without alarm.	Marks the fallure of an expected A/D end-of-conversion interrupt to occur.	Marks Insanity in the Logic Processor's working image of the most recent key-press data.	Marks the fallure of the CRC signature calculated over Logic Processor software to match that stored in ROM; causes the instrument to shut down without alarm.	Occurs when an error exceeding 1.5% in a sample of 50 motor revolutions is detected by the motion sensor.
Description	NOT USED	Error Log Reset	NOT USED	Battery Failure	A/D Failure	invalid Key	Startup ROM CRC fallures	Motor Sync Error
Code No.	00	01	02-05	90 5-33	20	80	09-12	13

	Logic Board Power Supply Board Pumping Mechanism		Battery Capacity Diminished Battery Charger Circultry Power Supply Board	Normal power-down sequence must occur to clear the condition. Logic Board Power Supply Board Battery	Logic Board Door Harness Assembly		AIL Board M.A.C.	AIL/Door Harness Logic Board		Logic Board
	Flagged when the motion sensor fails to confirm expected motor activity (because either the motion sensor is non-functional or the motor is not turning).		Marks a battery that is unable to hold a charge. After at least 1 hour of continuous operation on AC power, the instrument is required to be able to operate on battery for greater than one half the charge time; if unable to do so, error 18 is flagged: "HELP BATTERY" scrolls, audio alarm sounds, error codes 18 and 38 are logged, error code 38 is displayed in the VTBI LEDs, pumping stops, and all keys except PAUSE/STOP are disabled.	Occurs following a scenario where the instrument is powered down or reset without using the PAUSE/ STOP key (i.e., following watchdog or battery failure).	Occurs when a failure of the door sensor circuitry is detected.		Occurs when a failure of the AIL sensor is detected.			Marks the corruption of critical data expected to have been retained over power-down.
NOT USED	Sync Flag Failure	NOT USED	Fast Battery Discharge	Abnormal Power-Down	Door Circuitry Failure	NOT USED	AlL Circuitry or M.A.C. Failure		RESERVED FOR PC-1 TITRATION	Startup Inverse Flag Errors
14	15	16-17	8	19	20	21	22		23	24

									Attempt		
Logic Board	Power Supply Board		Battery Capacity diminished Battery Charger circuitry Power Supply Board		Logic Board	Logic Board	Logic Board		Battery excessively discharged. A recharge for 4 hours Lead Acid Battery Power Supply Board	Logic Board	Logic Board
Marks the corruption of data critical to un-	Occurs when a system battery voltage in excess of 8 VDC is detected.		Flagged if after a minimum charge time the instrument moves from Low Battery Level 1 to Low Battery Level 2 within 15 minutes; generates a Low Batt II (a.k.a. 'LB2') condition (see Error Code 38).		Marks stack overflow, underflow or stack pointer corruption in the stack used to control bank switching; generate watchdog conditions.	Occurs when the reference voltage for the A/D converter is found to be outside the range of 2.5V+/-5%.	Occurs when the main processor falls to detect at least 0.2V of audio activity following audio circuitry activation.		Occurs when less than ≈5.4V is read at the system battery; generates the following I/O: "HELP BATTERY" scrolls, audio alarm sounds, pumping stops, the VTBI LEDs display error 38, and all keys except PAUSE/STOP are disabled.	Marks a compound error condition with several contributing factors, all indicating that system software is not executing as programmed.	Marks corruption in the indicated NVRAM data partition. If the power-up validity check on any given partition fails, a partition-specific error code is logged and all data in the partition is re-initialized to default values. This error condition does not generate any alarm I/O.
Run-time Inverse lag	Battery Overcharge	NOT USED	Short LB1 to LB2 Time	NOT USED	Bank-switching Stack Errors	V Ref Error	Audio Failure	NOT USED	Low Battery (LB2)	General Software Error	NVRAM Partition Failure
25	26	27-28	29	30-32	33-34	35	36	37	<u>ස</u>	39	40-47

	Annual Property of the Control of th	Logic Board		Logic Board	Logic Board NiCad Battery	Logic Board	Power Supply Board Battery	Logic Board		Logic Board Display Board
The partition mapping is as follows: 40		Marks vectoring into a ROM bank other than 0 when the instrument is powered on; the error message 'ROMx rst' is displayed, where 'x' is one of 1, 2 or 3, to identify which bank was vectored into; the system goes into watchdog.		Occurs when the control variable critical to occlusion detection takes on an illegal value.	Occurs when the NiCad battery voltage is pulled abnormally low during controlled runtime load-testing.	Marks corruption in the Event History Log; the error code is logged and the event history is cleared. This error condition does not generate any alarm I/O.	Flagged when dual charging has exceeded eleven hours; the error code is logged but no further recovery actions take place.	Marks insanity in the audio control software; generates malfunction I/O, which may be followed by a watchdog condition.		Marks silence on the IPC serial link in excess of two seconds, where silence can be broken only by the receipt of well-formed messages containing valid data.
	NOT USED	ROM Bank Reset Errors	NOT USED	Occlusion Detection Error	NiCad Circuit Failure	Event History Reset	Dual Charger Error	Audio Control Error	NOT USED	IPC Silence
	48-49	50-52	53	54	55	56	57	58	59	09

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							_						
Logic Board	Logic Board	Logic Board	Logic Board NiCad Battery	Wrong Display EPROM Wrong Logic EPROM		Logic Board		Strain Beam Logic Board	Sticking pumping mechanism Logic Board	Logic Board	Logic Board		
Occurs when error log access/control software is asked to a process an error code it cannot recognize.	Marks a failure of the feedback circultry to register at least 0.2VDC during startup audio testing.	Indicates a request to switch to an unknown ROM bank; the error code is logged, then the system is forced into a watchdog condition.	Flagged when the NiCad battery is found to be below an acceptable threshold (1) during startup testing, or (2) during run-time testing after a controlled load has been applied.	Marks a mis-match between Logic and Display software version numbers.		Occurs on corruption of VersaTaper and AutoTaper time-tracking data.		Occurs when the main processor does not detect variance greater than or equal to 100 mV between the highest and lowest readings during any 2 revolutions of the pumping mechanism.	Occurs when the actual time required to complete a motor revolution differs from the expected time by ±12% for 3 consecutive revolutions.	Flagged when motor control data falls sanity cross-checks.	Marks a net calculation error in the motor table in excess of 200msec for a single revolution (implying the inability of the processor to perform arithmetic operations correctly).		
Illegal Error Code	Power-up Audio Fallure	Illegal ROM Bank Request	NVRAM Battery Failure	Software Version Error	NOT USED	TPN Time Error	NOT USED	Strain Beam Error	Motor Revolution Error	Rate Corruption Error	Motor Table Calculation error	NOT USED	
61	62	63	64	65	29-99	68	02-69	71	72	73	74	75	

Logic Board	Logic Board		Display Board Logic Board	In the event any of these errors is displayed: check NVRAM battery voltage after charging battery for 8 hours or longer,	ir still tow, replace battery; if within ilmits, replace Logic Board.			Logic Board	Logic Board		Display Board Logic Board	N/A	Logic Board Display Board
Flagged when a divide-by-zero operation is about to occur (the division is by-passed).	Occurs when cross-chacks preceding a power-down sequence fail; the error code is logged, then the system is allowed to go into watchdog.		Logged when key data in a Display-to-Logic Processor IPC message has falled an internal consistency check.	wing startup and run-tim n NVRAM data insanity:	Versa Taper Step Inverse Versa Taper Step Inverse Primary Rate Range Secondary VTBI Range Secondary VTBI Range	85 95 Total Vol Intused Hange Error 86 96 Sec Vol Inf Range Error 87 97 Motor step Range Error 88 Fractional Data Range Error		Marks the failure of the CRC signature calculated over Logic Processor software to match that stored in ROM.	Flagged on AutoTaper control and data Insanity.		The display processor has sent startup key data during operation	(Not errors)	Occurs when the Display Processor senses motor activity after the Logic Processor has indicated that there should be none, i.e., when the infusion rate has nominally been set to zero.
Divide-by-0 Error	Insane Power-Down	NOT USED	Insane Key Data	Critical Data Sanity Errors			NOT USED	Run-time ROM CRC Failures	AutoTaper Control and Data Errors	NOT USED	Unexpected Startup Key Data	IPC Event Tracking	Unexpected Motor Activity
92	7.7	82	79	80-89, 90-97	<u></u>	5-38 ===	66-86	100-103	104-110	111-113	114	115-117	118

Logic Board Display Board Logic Board	N/A	Logic Board	Display Board		Display Board	Display Board	Logic Board Display Board	Logic Board Display Board	Display Board	Display Board	Display Board	Display Board Logic Board	
Occurs when data destined for the rate or VTBI displays is out of range. Flagged on calls to unrecognized procedures during ROM bank switching operations.	(Not errors)	Mark fallures of ROM bank switching operations to activate the expected bank.	Occurs when the the Display Processor interrupt mechanis:n is found to be nonfunctional.		Flagged when the Display Processor has deliberately stopped strobing the watchdog circuitry in response to internal error conditions; places the system in watchdog with the error code in the VTBI display.	Marks insanity in the display Processor A/D circuit and/or its reporting mechanism.	Occurs on mis-matches between data reflecting the nominal pumping rate and the rate to be displayed; detected by the Display Processor.	Marks insanity in the control variable for the state machine on the Display Processor which monitors motor activity.	Flagged when the Display Processor has sensed a failure in either of its two levels of software processing.	Occurs when the Display Processor has found a variable controlling entry to a PL/M case' statement to be outside its legal range.	Marks a failure of the run-time 7-segment LED test.	Logged when the Display Processor senses a failure in the startup watchdog test.	
Rate/VTBI Display Data Error Illegal Bank-Switched Procedure Call	IPC event tracking	ROM Bank Switch Failures	Loss of the Display Processor Half-Millisecond Interrupt	NOT USED	Display Processor Watchdog	Display Processor A/D Error	Display Processor Rate Cross-Check Error	Display Processor Rate Monitoring Error	Display Processor Software Execution Error	Display Processor Case Error	LED Segment Error	Watchdog Test Failure	
119	124-125	126-129	130	131-134	135	136	137	138	139	140	141	142	

Display Board	Logic board	Display Board	Display Board Logic Board	Logic Board Display Board	Logic Board Display Board	Logic Board Display Board	Logic Board Display Board				
						 		Ν	N/A	Ν̈́	
Display Processor to the IPC mess		Marks the failure of the CRC signature calculated over Display Processor software to match that stored in ROM.	Flagged when the Display Processor has defermined that the motor revolution time is inappropriate for the infusion rate currently in effect.	Logged when the Display Processor has detected too little stepping activity for the motor speed reported by the Logic Processor.	Marks detection of an air-in-line condition by the Display Processor.	Logged when the Display Processor has detected an infusion to have continued at least 3 revolutions beyond the total number of steps indicated by the VTBI which was specified when the infusion was begun.	Flagged upon the receipt of illegal or insane data by the Display Processor over the IPC link (non-fatal, covered by redundancy built into the IPC protocol). 150 Display Proc. IPC Rate Data Error 151 Display Proc. IPC Motor Speed Error 152 Display Proc. IPC VTBI Data Error 153 Display Proc. IPC New VTBI Data Error 154 Display Proc. IPC Step Data Error	(Not errors)	Marks the receipt of Illegal or insane data by the Display Processor over the IPC (nonfatal, covered by redundancy built into the IPC protocol)	(Not errors)	
IPC Sequence Number		Rom CRC Error	Motor Revolution Error (Version 8.12 only)	Missing or Slow Steps	Air-in-Line Error	Overinfusion Error	Recoverable IPC Errors	IPC Event Tracking	(Recoverable) Display Processor IPC message CRC Error	IPC Event Tracking	
143		144	145-146	147	148	149	150-154	155-157	158	159-160	

"low battery 2" since the last time this minimum was reset (the maximum displayable value is 9999). In the case where "low battery 2" has never been reached, "----" appears in the VTBI display.

AUDIO TEST

- 1. Press COMPUTER CONTROL/MONITOR repeatedly until:
 - "Audio Test" scrolls on the Operator Information display.
- 2. Press START and check:
 - "no audio", "key", "variable", and "max" appear in the Operator Information display.
 - associated feedback voltages appear in the VTBI display.
 - an audible tone is sounded that increases in intensity as each message appears in the Operator Information display.

POWERDOWN TEST

- Press COMPUTER CONTROL/ MONITOR repeatedly until:
 - "Powerdown Test" scrolls on the Operator Information display.
- 2. Press START and check:
 - "OFF n" displays statically on the Operator Information display ('n' counts down in seconds from 3 to 1)
 - After reaching 1 the instrument shuts down.

MECHANISM ALARM CIRCUIT (M.A.C.)

Gemini PC-1 instruments are being manufactured with a pump mechanism alarm circuit (M.A.C.) to enhance detection of impact damage to the instrument as the result of dropping or other abnormal handling.

If a PC-1 pump/controller goes into an alarm condition and scrolls "HELP INTERNAL ERROR" following Power On, check the Error Code on the Central Display. These alarm conditions are

associated with either faulty AIL hardware or a disconnect in the pump mechanism alarm circuit. To determine the specific problem, perform the following troubleshooting procedures.

- a. Turn off the instrument and remove the AC power cord from the AC outlet.
- b. Separate the case (refer to Section 5.5.1).
 - Use a 5/32" Allen driver or wrench to remove the four socket head screws that connect the front and rear case assemblies. (Do not leave screws on work surface. Front panel could be damaged if front case is laid on the screws).

NOTE

When separating and positioning the front and rear cases for M.A.C. installation, ensure that no tension is applied to the harnesses connecting the case assemblies.

- Inspect the upper and lower M.A.C. assembly contacts on each pump mechanism for a disconnected condition.
- d. If the M.A.C. contacts (spring clips) are in place, inspect the M.A.C. wires and connectors for a cut or break.
- e. If the M.A.C. circuit is intact, then the alarm condition can be attributed to an AlL hardware problem. Follow maintenance manual procedures for troubleshooting, removal and replacement of the AlL/SCD assembly.
- f. If the M.A.C. contacts are disconnected, the instrument has been subjected to an abnormal impact condition. Visually inspect the pump mechanism(s) for severe cracks or breaks in the areas around the mounting flanges and housing pivot points. Small cracks do not affect functionality.
- g. If no visual damage is detected, attempt to move the top of the pump mechanism laterally (side to side). If the mechanism is intact, there will be very little, if any, lateral motion. If the mechanism is broken, the lateral movement will be easily discernible.

NOTE

Do not mistake movement of the top of the pump mechanism along the hinge axis as

	COMPUTER CONTROL/ MONITOR switch failure	Replace Keypad
Model/Version fails to scroll	Digital Logic Failure Display Board Failure Power Supply Board Failure	Replace Digital Logic Board Replace Display Board Replace Power Supply Board
Model/Version display corrupted	Digital Logic Failure	Replace Digital Logic Board
LAMP TEST w/AUDIO	Display Board Failure	Replace Display Board
LED segment fails to illuminate	Display Board Failure Digital Logic Failure	Replace Display Board Replace Digital Logic Board
No Audio adjust	Audio Control Pot. Failure	Replace Audio Control Pot.
KEYPAD TEST		
Key/Display Mismatch or Invalid Key	Keypad Failure	Replace Keypad Assembly
	Display Board Failure	Replace Display Board
ERROR LOG DISPLAY (See Tal	ole 5-2, 2a or 2b for a listing and desc	ription of Error Log Codes)
MOTOR HOMING TEST		
Motor Fails to Home to selected position	Motor Harness Disconnected	Reconnect Motor Harness
±1 sîep	Digital Logic Failure	Replace Digital Logic Board
	Motion Sensor Hamess Disconnected	Reconnect Motion Sensor Harness
	Motion Sensor Failure	Replace Motion Sensor
PUMP TEST (Allows pumping mech	anism to be operated without Alarm st	oppage)
SERIAL PORT TEST	· · · · · · · · · · · · · · · · · · ·	
"echo" test fail	Faulty Communication Plug	Replace Comm Emulator Plug
	Digital Logic Board Failure	Replace Digital Logic Board
	Power Supply Board Failure	Replace Power Supply Board
A/D VOLTAGE DISPLAY		
"strain" reading >'0', set not installed	Strain Beam Out of Calibration	Recalibrate Strain Beam (see Section 5.7)
	Strain Beam Failure	Replace Strain Beam
	Digital Logic Board Failure	Replace Digital Logic Board
		l

	Digital Logic Board Failure	Replace Digital Logic Board			
only (except V8.12). ECD Sensor - wrong digit for condition	Light Emitter/Receiver Failure	Replace ECD			
Condition	Communication Cable Failure	Reconnect or Replace Commication Cable			
	Power Supply Board Failure	Replace Power Supply Board			
	Digital Logic Board Failure	Replace Digital Logic Board			
SCD Sensor - wrong digit for condition	Light Emitter/Receiver Failure	Replace AIL/SCD Assembly			
Condition		Analog Circuit Failure Replace AIL/SCD PC Board			
	Digital Logic Failure	Replace Digital Logic Board			
Motion Sensor - wrong digit for condition	Sensor Failure	Replace Sensor			
wrong digit for condition	Digital Logic Board Failure	Replace Digital Logic Board			
Door Sensor - wrong Digit for condition	Sensor Failure	Replace Sensor			
Digit for condition	Digital Logic Board Failure	Replace Digital Logic Board			
	NOTE				
	In the 'Selftest' mode, the microprocessor is strobing the sensors in accordance with a software protocol. The digital presentation seen in the RATE and VTBI displays reflects the sensor response to the strobe. If the response is not the expected response, a problem exists within the strobe circuitry.				
RAM Display	For IMED Engineering use only				
POWER DOWN TEST					
Displays remain On	Digital Logic Board Failure	Replace Digital Logic Board			